

Advertisement

Applications are invited for One post of Project Engineer at the Department of Electronic and Instrumentation Science, Savitribai Phule Pune University, at "Space Payload Development Center (SPDC)" funded by Savitribai Phule Pune University.

Number and Name of the post : One, Project Engineer
Category of post : Open to All
Fellowship : Rs. 31,000/+ HRA=38,440.
Duration of the project : Till 31st March 2027.

Number and Name of the post : One, Project Assistant
Category of post : open to All
Fellowship : Rs. 30,000
Duration of the project : Till 31st March 2027

The project is aimed at designing a payload to be deployed on the PSLV fourth stage (PS4 platform). The Project staff is expected to work on Payload Chassis design and sub system design & testing. Additional experiments related to testing & optimization of Payload design and thermal vacuum analysis of the entire structure will also be carried out.

Eligibility and Necessary Qualification for Project Engineer & Project Assistant:

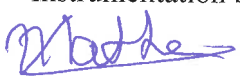
Project Engineer : M.Sc. in Electronic Science with good knowledge of Mechanical design and modelling, simulations OR BE Mechanical/Mechatronics /Production with experience in CAD modeling using Softwares like Catia & Solidworks.

Project Assistant : M.Sc. in Electronic Science / Physics / BE E&TC with experience in RF and antenna design/ FPGA based system Design/ DSP. Research/work experience in the field of Antenna, RF design/ FPGA based system design is desirable.

Plain-paper application, including a covering letter and a resume/CV, addressed to Prof V.L Mathe, Head, Department of Electronic and Instrumentation Science, Savitribai Phule Pune University, Pune 411007, should be submitted in the Office on or before 10th October 2026 sent via email to officedoes@gmail.com. Resume should include: full name, contact information (address, email, and contact number), date of birth, category, educational qualifications and relevant experience.

Shortlisted candidates will be sent an email regarding the interview details.

The interviews will be held online on 12th October 2026 at 11 a.m. in the Department of Electronic and Instrumentation Science, S.P. Pune University.


Head
Dept. of Electronic & Instrumentation Science,
SP Pune University, Pune 411 007


Registrar
Savitribai Phule Pune University
Pune 411007

Head, Dept of Electronics & Instrumentation Science
Savitribai Phule Pune University
Pune - 411 007.


Assistant Registrar
Planning & Development Section
Savitribai Phule Pune University


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