

## Advertisement

Applications are invited for two Project Assistants, at the Department of Electronic Science, Savitribai Phule Pune University, to work on project entitled

### Development of Pre qual engineering model of "SEAPS" (300 KHz to 30 MHz) RF Front-End Electronics and Data Acquisition System for low frequency space science studies

Name of the post:	Project Assistant
Number of posts	Two
Category of post:	One Open, One SC
Fellowship:	Rs. 18,000 pm

Duration of the project : Upto 31<sup>st</sup> March 2018

The Space Electromagnetic And Plasma Sensors (SEAPS) is a proposed payload, aimed at the study of space plasma interactions in space. The project assistants are expected to work on RF and Analog front end design like LNA's, Filters, Pre amplifiers etc and FPGA based system design, Data acquisition, signal conditioning, FFT implementation etc.

**Eligibility and Necessary Qualification for Project Assistants :** M.Sc. in Electronic Science/Physics/ Instrumentation Science/ BE E&TC with experience or M. Tech. Good academic record and NET/SET/Gate qualified candidates will be given preference. Research/work experience in the field of RF design/ FPGA based system design is desirable. Considering the time bound nature of the project, if suitable SC candidate is not found for the post, a candidate from another category will be appointed as a Project assistant.

Plain-paper application, including a covering letter and a resume/CV, addressed to Dr. Mrs. D.C. Gharpure, Department of Electronic Science, Savitribai Phule Pune University, Pune 411007 should be submitted in the Office on or before 19/9/2017 or sent via email to [dcg@electronics.unipune.ac.in](mailto:dcg@electronics.unipune.ac.in).

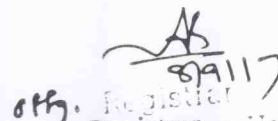
The interviews will be held on 19<sup>th</sup> September at 11.00 am in the Department of Electronic Science Savitribai Phule Pune University.

Resume should include: full name, contact information (address, email, and a contact number), date of birth, educational qualifications and relevant experience.



Head  
Department of Electronic Science,

सवित्रीबाई फुले पुणे विद्यापीठ

  
Registrar  
University of Pune