

Savitribai Phule Pune University

Department of Technology

M.Tech, Integrated M.Tech Ph.D. (Electronics and Electrical Technology)

Syllabus for Entrance Examination

Section – A

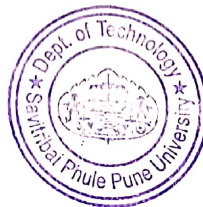
General Aptitude

- Test of Reasoning
- Verbal Analogies
- Numerical Ability
- English Grammar
- Verbal Deductions
- Word Groups
- Structural Orientation

Section – B

- As per GATE syllabus of Electronics & Communication Engineering (EC) and Electrical Engineering (EE) having equal weightage for both.

*For Integrated M.Tech Ph.D.(Electronics and Electrical Technology) objective questions on Research Aptitude will be included in addition to the above syllabus.



Head
Department of Technology
Savitribai Phule Pune University
Pune - 411 007.

